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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Chih Hsin Wang et al.

Group Art Unit: 2814

Serial No.: 10/690,204

Examiner: Marcos P. Pizarro Crespo

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Gray Cary Ware & Freidenrich LLP
2000 University Avenue
East Palo Alto, CA 94303-2248

Title: A Semiconductor Memory Array Of Floating
Gate Memory Cells With Low Resistance
Source Regions And High Source Coupling

Customer No. 26379

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as FIRST CLASS MAIL in an envelope addressed to: MS ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on:
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Kathleen K. Bane

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SUBMISSION OF FORMAL DRAWINGS

Attn.: Official Draftsperson

MS Issue Fee

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby submits twenty (20) sheets of formal drawings in the above-identified patent application.

Please charge any fees for the submission of the formal drawings to Gray Cary Ware & Freidenrich LLP, deposit account 07-1896. A duplicate copy of this transmittal is enclosed if charge of additional fees is necessary.

GRAY CARY WARE & FREIDENRICH LLP

Dated December 16, 2004

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